

## NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE **BOARD OF PATENT APPEALS AND INTERFERENCES**

F/2800

PATENT APPLICATION

Inventor(S)

Frank Hui Yifeng Yan

Allen Yen

Case:

3-6-16

Service "First Class Mail" on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450,

I hereby certify that this Notice of Appeal is being deposited with the United States Postal

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Serial No.

09/653,295

Group Art Unit: 2815

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Filing Date

08/31/2000

Examiner: Nguyen, Cuong

Title:

Stacked Structure For Parallel Capacitors And Method of Fabrication

ASSISTANT COMMISSIONER FOR PATENTS **WASHINGTON, D. C. 20231** 

SIR:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated August 4, 2003 of the Primary Examiner finally rejecting claims: 1-6.

The item(s) checked below are appropriate:

[] A Petition for Extension of Time for reply to the rejections is attached.

[] Please charge the amount of \$330.00 covering the Appeal Fee (37 CFR 1.17(2)), to Agere Systems Deposit Account No. 501735. Duplicate copy of this letter is enclosed.

[X]Appeal Fee not required. (Fee was paid in prior Appeal.)

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 501735** as required to correct the error.

Please address all correspondence to Docket Administrator, Agere Systems Inc., 4 Connell Drive, Room 4U-533C, Berkeley Heights, NJ 07922-2747.

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Date: 3 November 2003

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